

Title of the Invention: Emitter Turn-Off Thyristors (ETO)
Inventor's Name: Huang
Docket No./Application No.: 09/486,779 3/24 FEB 2 6 2004 A RADEMA FIG.2B CATHODE(42) 32 PMOS2(44) \$ FIG.2A N DRFT + d ASOE CATE (48) 껆 P BASE S PM0S1(32) \$ \(\)

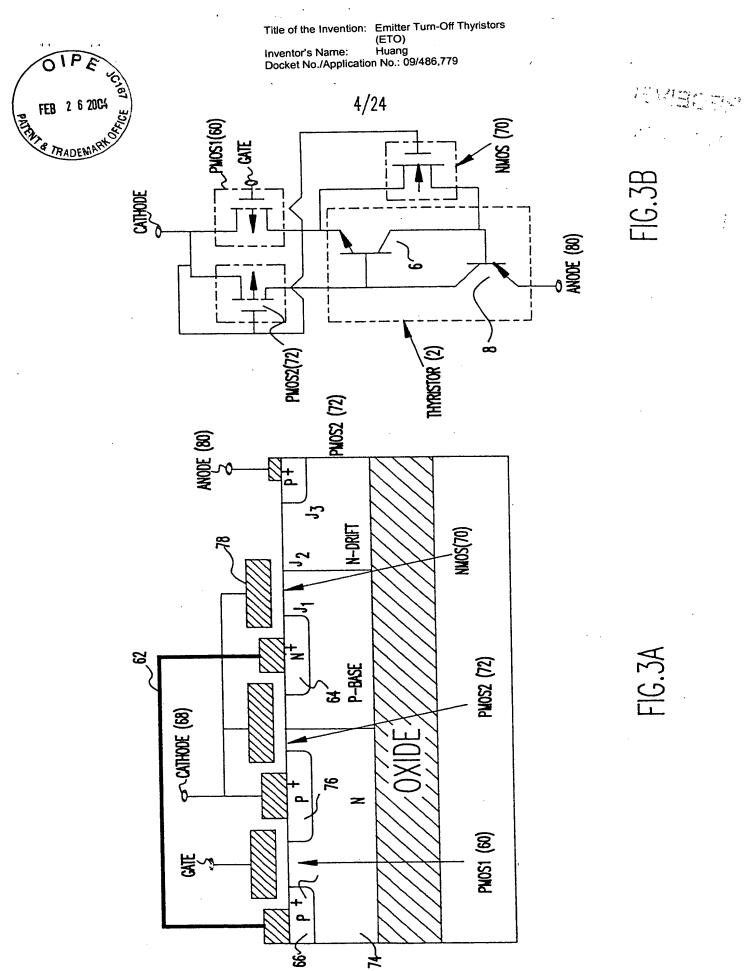
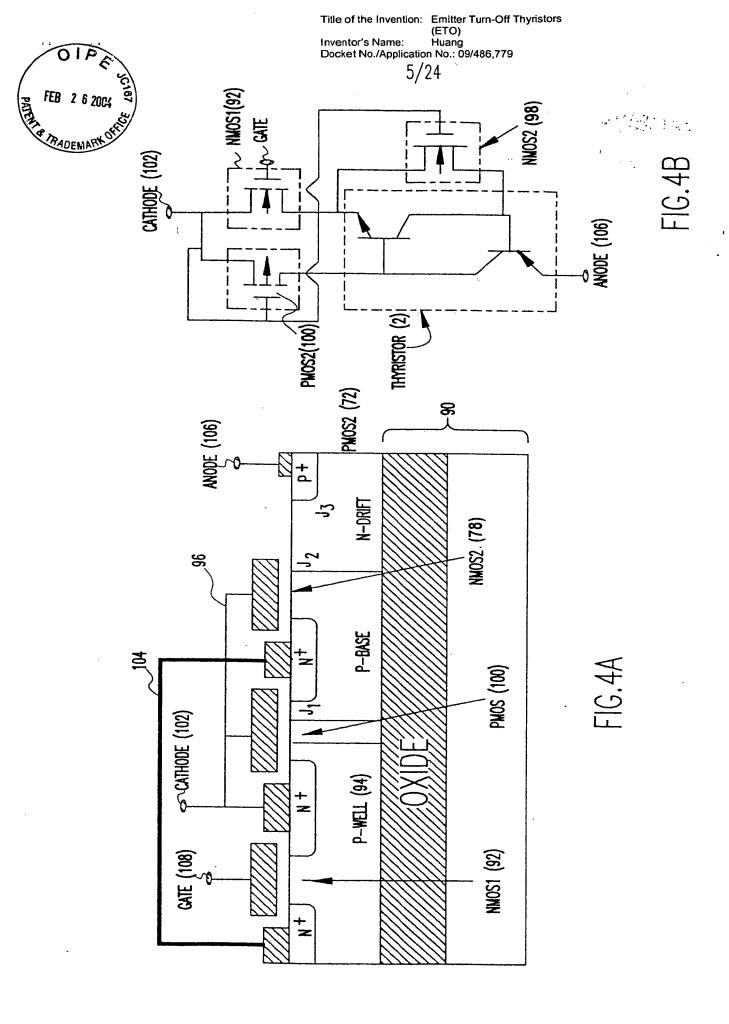


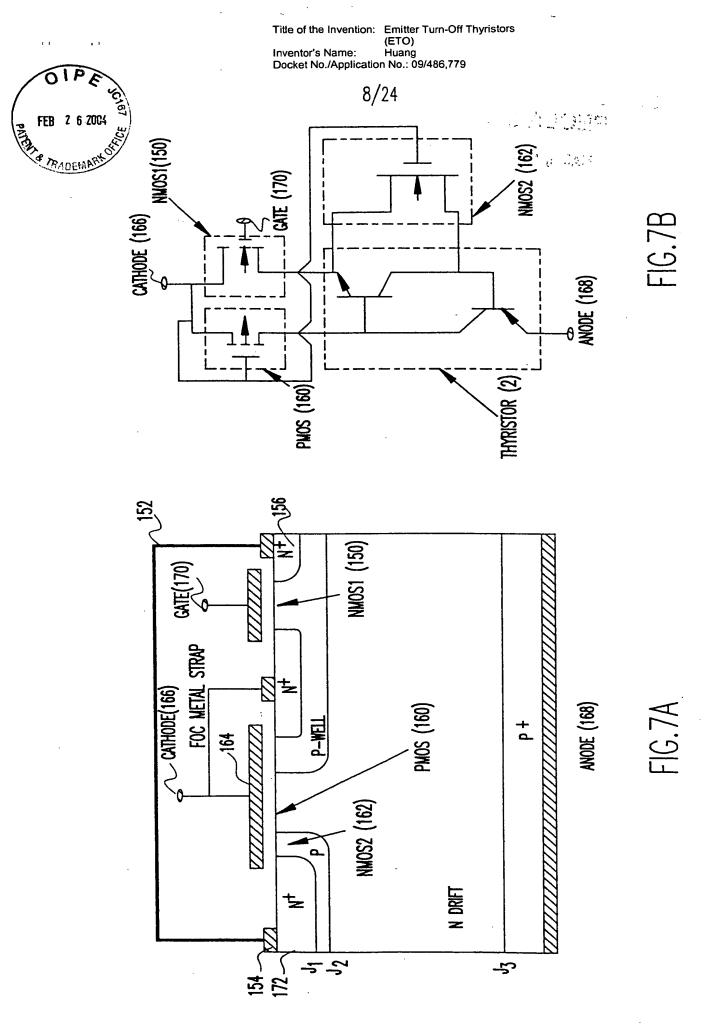
FIG.3A

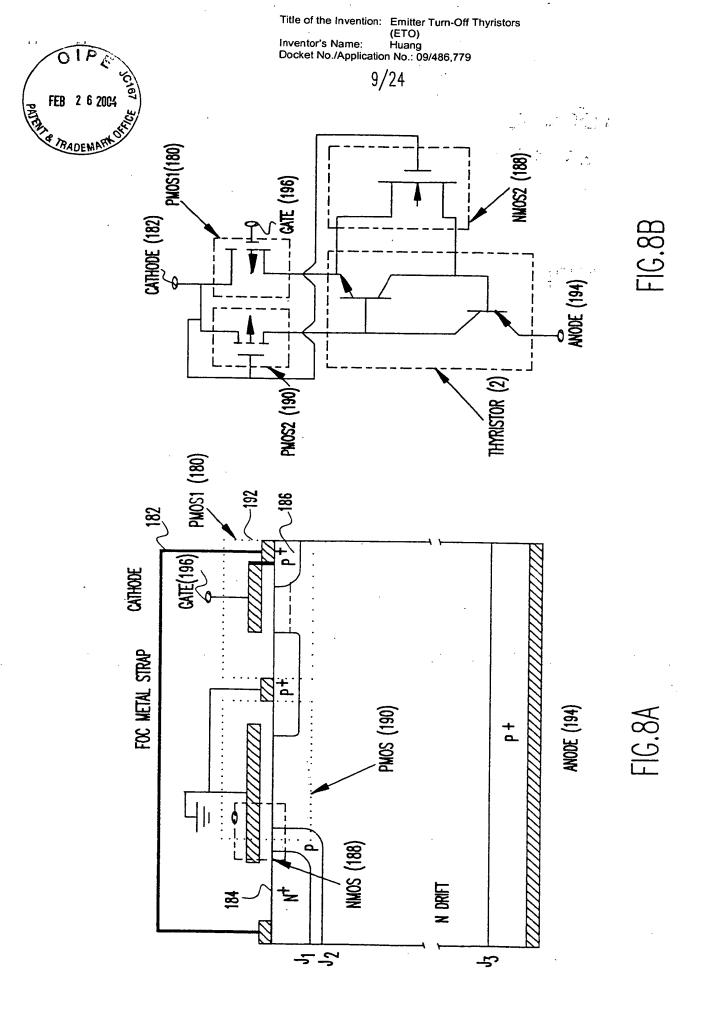


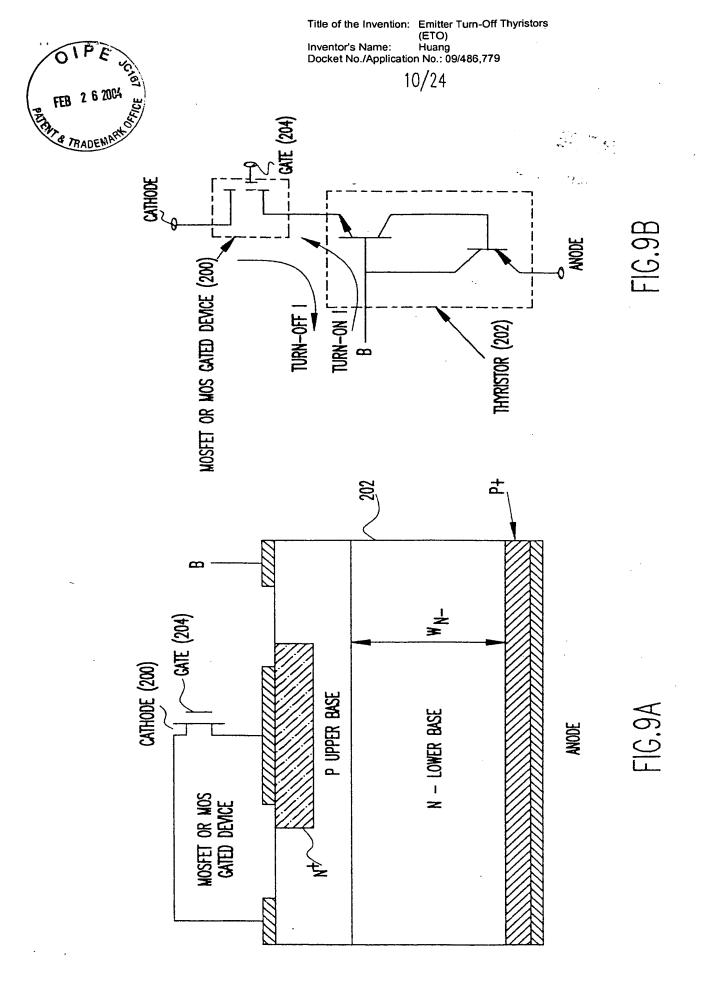
Title of the Invention: Emitter Turn-Off Thyristors (ETO)
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Docket No./Application No.: 09/486,779 6/24 FEB 2 6 2004 A PADEMARY ~ PMOS1(110) CATHODE (116) FIG.5B ANODE (124) THYRISTOR (2) N-DRIFT PMOS1 (110) FIG.5A CATHODE (116) GATE (126) 5 \$ NMOS (118) PMOS2 (120) P-BASE

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Docket No./Application No.: 09/486,779 7/24 CATHODE PHIOS1(130) FIG.68 ANODE (146) THYRISTOR (2) PMOS1 (130) **CATHODE(138)** ANODE (146) FIG.6A PMOS2 (140) P BRSE با ا ೭

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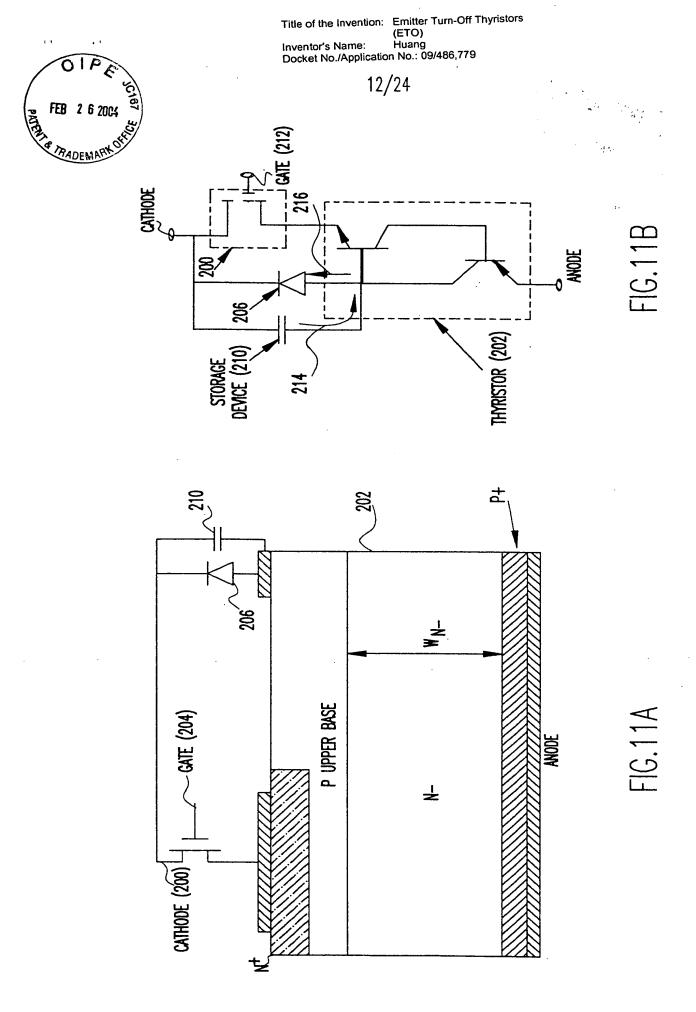


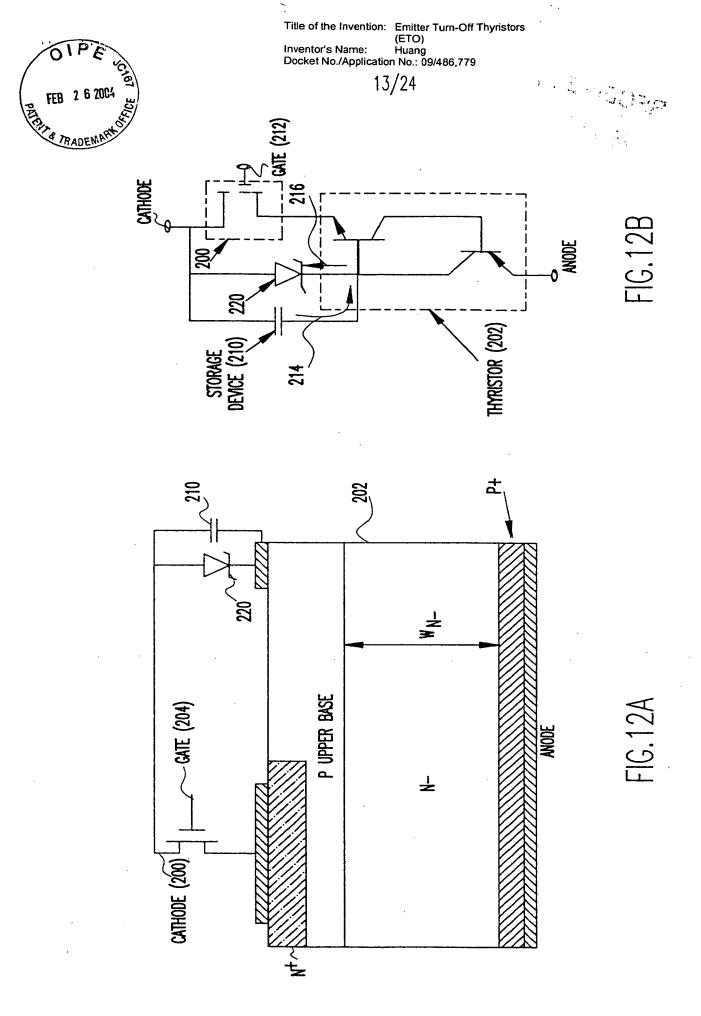


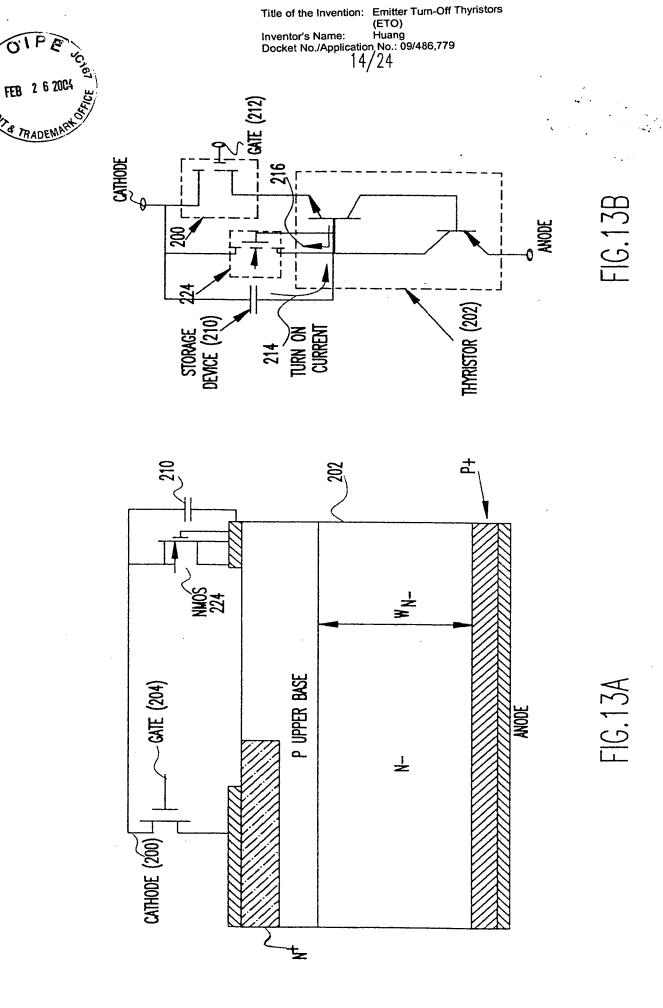
Title of the Invention: Emitter Turn-Off Thyristors (ETO)
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Docket No./Application No.: 09/486,779 11/24 TURN-OFF I THYRISTOR (202) TURN-ON I FIG. 10A P UPPER BASE ANODE

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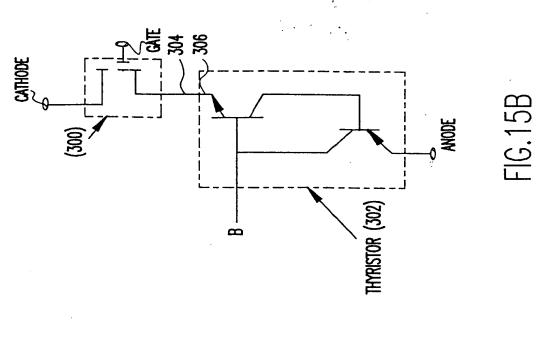
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Inventor's Name: Huang
Docket No./Application No.: 09/486,779

15/24 FEB 2 6 2004 PADEMARY CATHODE FIG.14B THYRISTOR (202) -N FIG.14A P UPPER BASE ł

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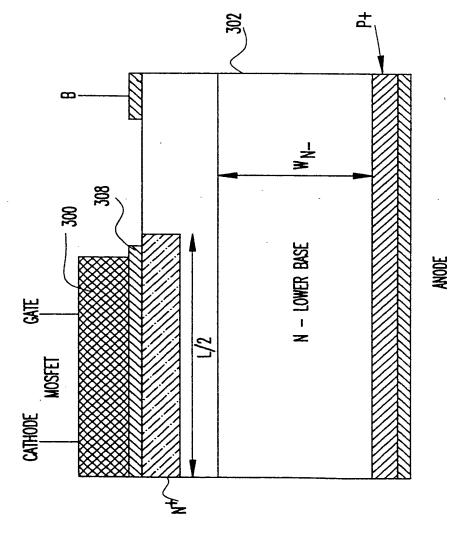
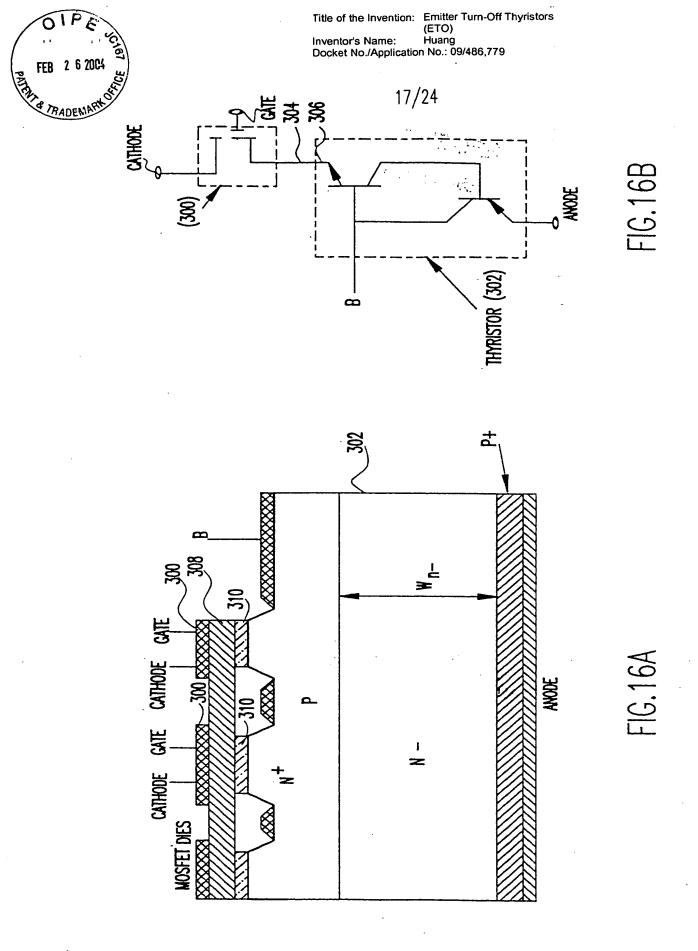
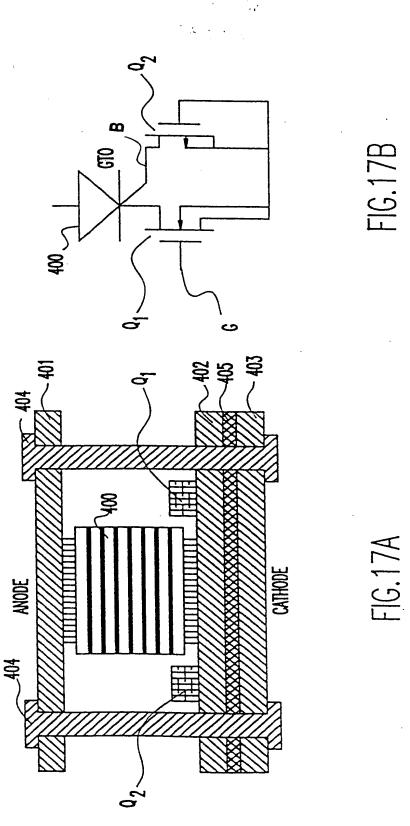


FIG. 15A



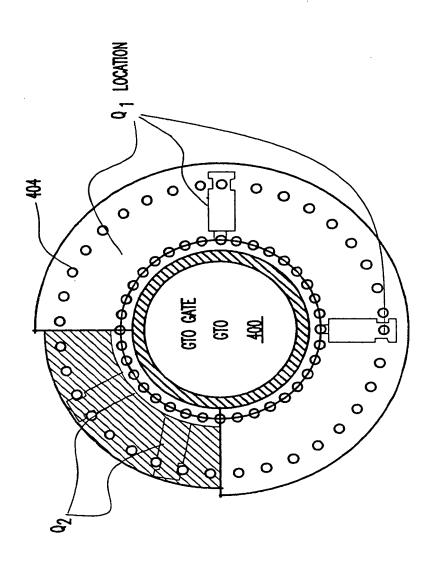
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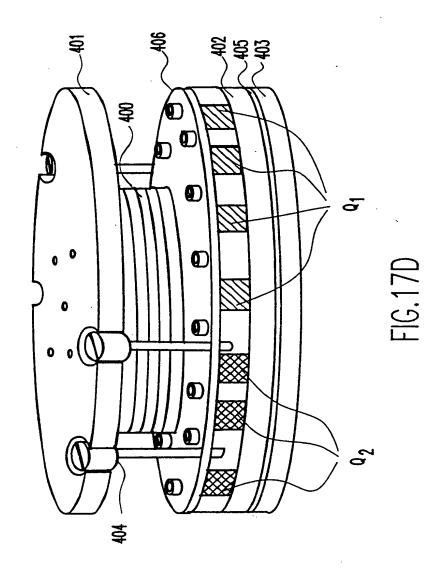
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ETO1040S Turn-On

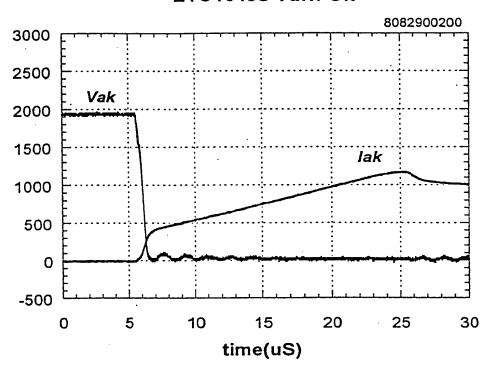


FIG.17E



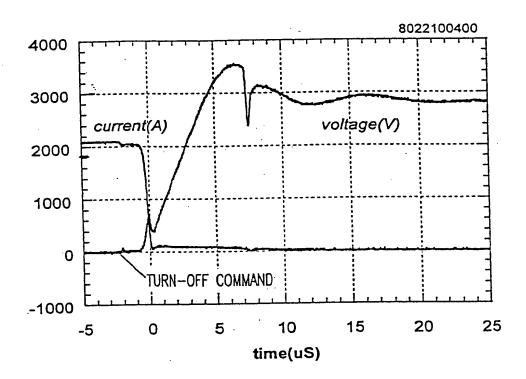
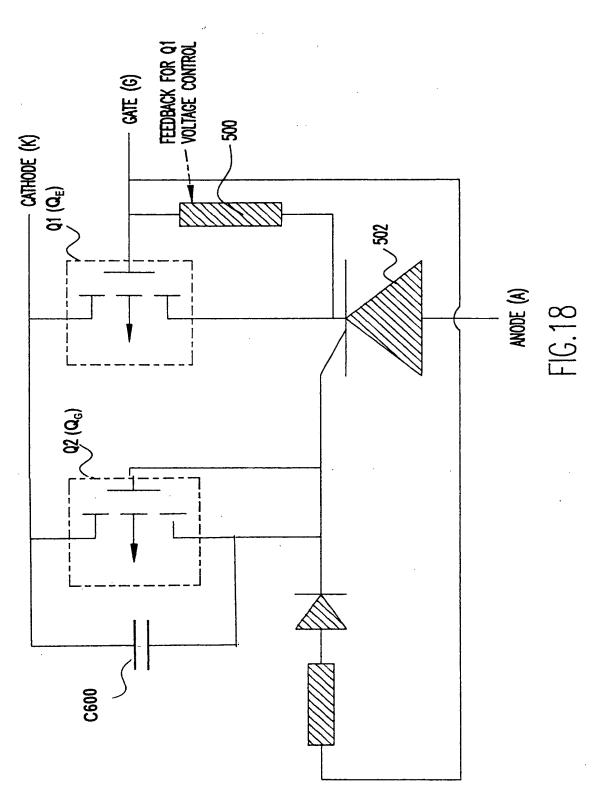


FIG.17F

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nventor's Name: Huang
Docket No./Application No.: 09/486,779 24/24 — CATE (G1)
FEEDBACK FOR Q1 VOLTAGE
CONTROL FEB 2 6 2004 ____ САТНОDE (K) ANODE (A) 8